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JC525 U.S. PRO
09/15/00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned
Examiner: To Be Assigned

In re PATENT APPLICATION of

Applicant(s) : Yasuo TANAKA

)

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Appln. No. : To Be Assigned

)

) **INFORMATION**

Filed : September 12, 2000

) **DISCLOSURE**

For : METHOD OF MANUFACTURING
SEMICONDUCTOR DEVICE

) **STATEMENT**

Atty. Dkt. : OKI 262

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Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

FEE ENCLOSED: \$0.00
N-18-0002
Please charge any further fees to our Deposit Account

Attached are copies of two Japanese-language publications. The relevance of these documents can be determined from page 1 of the specification. The publications are listed on the attached Form PTO-1449.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the document submitted. Consideration of the submitted document is respectfully requested.

Respectfully submitted,



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September 12, 2000

Date

SMR:lep